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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Chadwick et al.			Docket No. BUR920010195US1
Application No. 10/065,694	Filing Date 11/11/2002	Examiner James C. Kerveros	Group Art Unit 2133
Invention: METHOD FOR TESTING EMBEDDED DRAM ARRAYS			
I hereby certify that this			
on 12/2/20 (Date)	105		
(Typed or Printed Name of Parson Signing Certificate) (Signature)			
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DEC 0 2 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/065,694

Applicant(s): Chadwick et al.

Filed.: November 11, 2002

Art Unit: 2133

Dkt. No.: BUR920010195US1

Examiner: James C. Kerveros

Title: METHOD FOR TESTING EMBEDDED DRAM ARRAYS

Honorable Commissioner for Patents P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed on September 6, 2005.